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LCD MODULE SPECIFICATION FOR CUSTOMER'S APPROVAL

CUSTOMER •

MODULE TYPE: <u>NMTG-S12864BFYHSGY-D</u>

APPROVED BY: (FOR CUSTOMER USE ONLY)





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Revise Records

Rev.	Date	Contents	Written	Approved
Α.	2014/06/06	Initial Edition	Jill Hsu	Danny Lien

Special Notes

Special No		
Note 1.	1.	Change PCB supplier, Change backlight supplier.
1,010 1.	2.	Change Model name to NMTG-S12864BFYHSGY-D.
Note 2.		
Note 3.		
Note 4.		
Note 5.		
	1	
	1	



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SPECIFICATION FOR

LIQUID CRYSTAL DISPLAY MODULE

MODEL NO. : <u>NMTG-S12864BFYHSGY-D</u>

View Direction	☑ 6 O'clock				□12 O'clock					
I CD Turo	□ FSTN Positive				□ FS	TN	Neg	gative		
LCD Type	□ STN G	ray	⊠ S	STN Yellow Green			Blue			
Rear Polarizer		tive	1	☑ Transflective				Trans	missive	
Backlight Type	☑ LED	□ EL		\checkmark	Inter	nal Pov	wer		☑ 5V	V input
Backlight Type			FL		Exter	rnal Power		\Box 24V input		4V input
Backlight Color	□ White		mber	•		Blue Green ☑		Yellow Green		□ Other
Temperature Range	□ Norma	□ Normal		☑ Wide				□ Super Wide		er Wide
EL Driver IC	□ Build-in			☑ Not Build-in						
Touch Screen	□ With			☑ Without						
LCD LSI	SBN640C	G21 / SB	N00	640	G-D					

TO BE VERY CAREFUL !

The LCD driver ICs are made by CMOS process, which are very easy to be damaged by static charge, make sure the user is grounded when handling the LCM.



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GENERAL SPECIFICATION

Item	Content
Display Resolution	128(W)×64(H)
Dimensional Outline(mm)	75.0(W)×52.7(H)×9.0max(D)
Dot Size	0.40(W)mm×0.40(H)mm
Dot Pitch	0.43(W)mm×0.43(H)mm
Display mode	Transflective/ Positive Type
Circuit	Avant IC
Interface	Data (D0~D7), D/I, R/W, E, RST, CS1, CS2, V _{EE}

ABSOLUTE MAXIMUN RATING

Item	Symbol	Min.	Max.	Unit	Note
Power Supply for Logic	$V_{DD} - V_{SS}$	-0.3	7.0	Volt	
Power Supply for LCD	$V_{DD} - V_{EE}$	-0.3	19.0	Volt	
Input Voltage	VI	-0.3	V _{DD} +0.3	Volt	
Current for LED backlight			200	mA	
Static Electricity					Note 1

Note 1: Operator should be grounded during handling LCM.

(2) Environmental Absolute Maximum Ratings

	Normal Temperature				Wide Temperature				
Item	Operating		Storage		Operating		Storage		
	Max,	Min.	Max,	Min.	Max,	Min.	Max,	Min.	
Ambient Temperature	0°C	+50°C	-20°C	+70°C	-20°C	+70°C	-30°C	+80°C	
Humidity(without condensation)	Note 2,4		Note 3,5		Note 4,5		Note	e 4,6	

Note 2: Ta \leq 50°C : 80% RH max

Ta>50°C : Absolute humidity must be lower than the humidity of 85%RH at 50°C

Note 3: Ta at -20° C will be<48hrs at 70° C will be <120hrs when humidity is higher than 75%.

Note 4: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 5: Ta \leq 70°C : 75RH max

Ta>70°C : absolute humidity must be lower than the humidity of 75%RH at 70°C Note 6: Ta at -30° C will be <48hrs, at 80 °C will be <120hrs when humidity is higher than 75%.



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ELECTRICAL OPTICAL

Characteristics Ta = 25°C								
D (Rating			Test		
Parameter	Symbol	Min.	Тур	Max.	Unit	Condition		
Total Forward Voltage	VF	3.8	4.05	4.3	V			
Average Luminous Intensity	IV	18	25		cd/m ²			
Luminous Tolerance	IV-m	50			%			
Chromaticity Coordinate *	Х	0.4000	0.4441	0.4878				
Chromaticity Coordinate *	у	0.5906	0.5547	0.5152				

Note1: This product must be in VF MAX. under the testing.

Note2: The current should be set at 2x50mA when the finished goods is under the testing.

BACKLIGHT ABSOLUTE MAXIMUM RATING

 $Ta = 25^{\circ}C$

			14 250
Parameter	Symbol	Rating	Unit
Power Dissipation	Ро	860	W
Peak Forward Current	IF	200	mA
Reverse Voltage	VR	8	V
Operating Temperature Range	TOPR	-20° C TO $+ 70^{\circ}$ C	
Storage Temperature Range	TSTO	-30° C TO $+ 80^{\circ}$ C	



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Item	Symbol	Condition	Min.	Тур	Max.	Unit
Power Supply for Logic	$V_{DD} - V_{SS}$		4.5	5.0	5.5	Volt
Legent Valta as	V _{IL}	L level	0		0.8	Volt
Input Voltage	V_{IH}	H level	V _{DD} -2.2		V_{DD}	Volt
Power Supply	I _{DD} (LED B/L OFF)			1.5	1.8	mA
Current for LCM	$I_{\rm EE}$	$V_{DD} = 5.0V$ $T_a = 25^{\circ}C$		0.6	1.0	
LCM	I _{LED}	$V_{DD} - V_{EE}$ = 10.3V		100	150	
Power Supply for EL Backlight	\mathbf{V}_{EL}	$V_{LED} = 5.0 V$		100V/ 400Hz		

ELECTRICAL CHARACTERISTICS

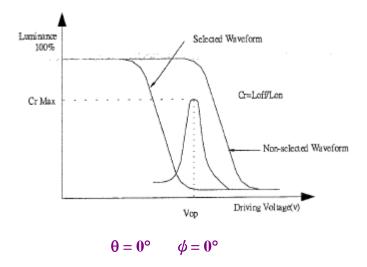
OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Тур	Max.	Unit	note
LCM Recommend		$T_a = -20^{\circ}C$	9.14	9.64	10.14		
LCD Module	Vop	$T_a = 25^{\circ}C$	8.60	9.10	9.60	Volt	1.
Driving Voltage		$T_a = 70^{\circ}C$	8.06	8.56	9.06	-	
Viewing	Front-Back	θ=0°	30	92		deg.	3.
Angle	Left-Right	θ=0°	60	90		deg.	3.
Rise Time	Tr	V _{DD} - V _{EE}		90	220	- S	2.
Fall Time	T _f	=10.3V		210	200	mS	∠.
Contrast	Cr	$Ta = 25^{\circ}C$	2.0	6.0			4

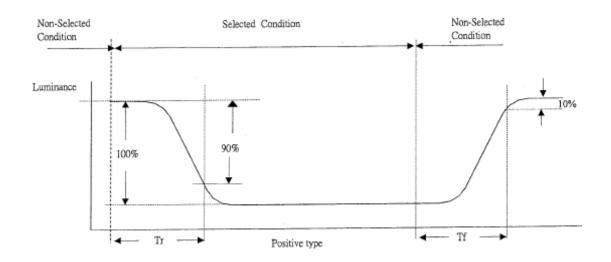


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[Note 1] Definition of Operation Voltage (Vop)



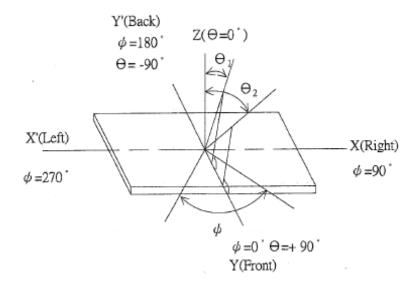
[Note 2] Definition of Response Time (Tr, Tf)



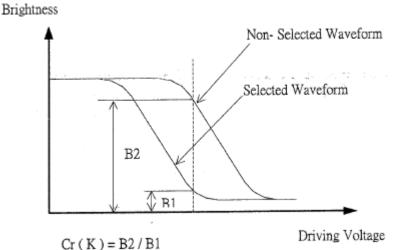


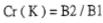
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[Note 3] Definition of Angle $\theta \& \phi$



[Note 4] Description of Contrast Ratio (K)







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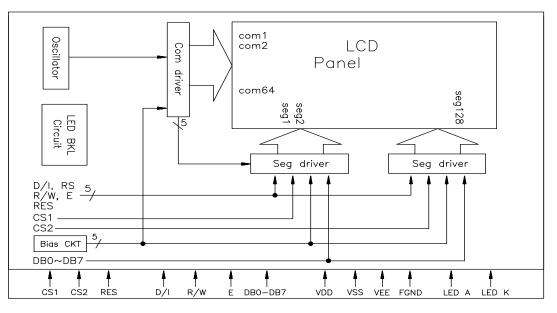
INTERFACE PIN ASSIGNMENT

Pin No.	Pin Out	I/O	Description
1.	V_{DD}		Logic supply voltage
2.	V _{SS}		GND
3.	\mathbf{V}_{EE}		Supply Voltage for LCD panel.
4.	DB0		
11.	DB7	I/O	Data bus. 3-state I/O common terminal.
12.	/CS1	Ι	Chip-select for the left half of the display. Active LOW.
13.	/CS2	Ι	Chip-select for the right half of the display. Active LOW.
14.	/RST	Ι	 Setting the RES signal to Low level can initialize the following registers. 1. ON/OFF register 0 set(Display off) 2. Display start line register 0 set(display starts from line 0) After releasing reset, this condition can be changed only by software.
15.	R/W	Ι	Read/Write R/W=high : Data of DB0~DB7 can be read by CPU. R/W=low : Data of DB0~DB7 can be written into LCD driver IC at the falling edge of E when CS1 and CS2 is high.
16.	D/I	Ι	Data/Instruction D/I=high : Indicates that data of DB0~DB7 is display data. D/I=low : Indicates that data of DB0~DB7 is instruction.
17.	Е	Ι	Enable When write(R/W=low) : Data of DB0~DB7 is latched at the fall of E When read(R/W=high) : Data is read while E is at high level.
18.	FGND		Frame Ground
19.	BKL _A		Power supply for backlight.
20.	BKL _K		(4.2V/100~150 mA DC for LED backlight, 110V/400Hz AC for EL)

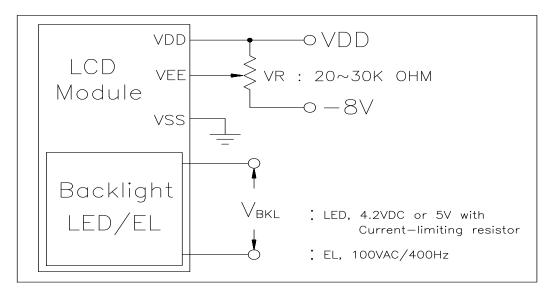


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BLOCK DIAGRAM



POWER SUPPLY



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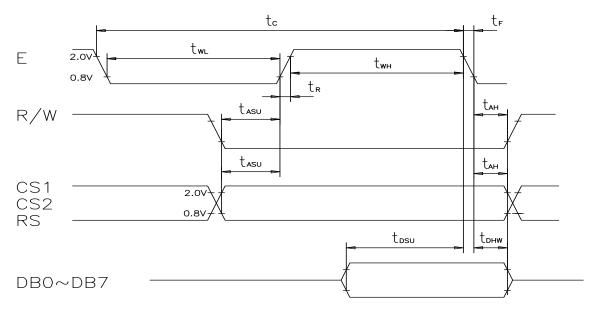
TIMING CHARACTERISTICS

MPU interface timing:	$(V_{SS} = 0V, V_{DD} = 4$	$4.5V \sim 5.5V$, $T_a = -20$ to 60°	2)
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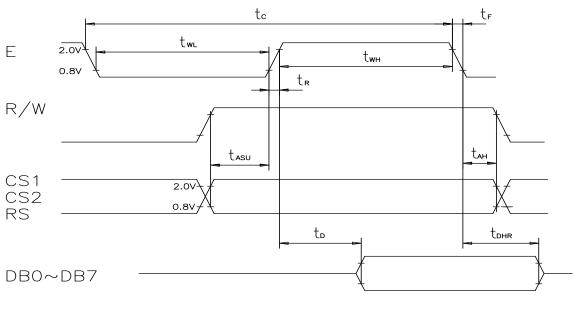
Item	Symbol	Min.	Typ.	Max.	Unit
E Cycle Time	t _C	1000			ns
E High Level Width	$t_{\rm WH}$	450			ns
E Low Level Width	t_{WL}	450			ns
E Rise Time	t _R			25	ns
E Fall Time	t _F			25	ns
Address Setup Time	t_{ASU}	140			ns
Address Hold Time	t _{AH}	10			ns
Data Setup Time	t _{DSU}	200			ns
Data Delay Time	t _D			320	ns
Data Hold Time(Write)	t _{DHW}	10			ns
Data Hold Time(Read)	t _{DHR}	20			ns



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DISPLAY COMMANDS

The display commands shown below control the internal state of the LCD driver ICs. Commands are sent from CPU to LCD module for the display control.

Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	0	0	0	0	1	1	1	1	1	1/0	To control the display ON or OFF. The internal status and display RAM data are not affected. 0:OFF, 1:ON
Set address (Y address)	0	0	0	1		Y	addres	ss (0~0	53)		To set the Y address in the Y address counter.
Set page (X address)	0	0	1	0	1	1	1	Ра			To set the X address at the X address register.
Display Start Line	0	0	1	1				To indicate the display data RAM displayed at the top of the screen.			
Status Read	0	1	Busy	0	ON/ OFF	Rese t	0	0	0	0	To read status of the LCD controller IC: Busy 0:Ready, 1: In operation ON/OFF: 0:Display ON, 1:Display OFF Reset: 0:Normal, 1:Reset
Write display data	1	0		Write Data				To write data into display data RAM. Y address is increased by 1 after this command.			
Read Display data	1	1				Read	Data				To read data from display data RAM to the data bus.



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MEMORY MAPPING

		RAM Y address(Y0 ~Y127)	Data
	Line $0 \rightarrow$	0 1 1 1 0 0 0 0 1 0 0 0	←DB0(LSB)
\bigcirc	Line $1 \rightarrow$	1 0 0 0 1 0 0 0 1 1 0 0	←DB1
X	Line $2 \rightarrow$	1 0 0 0 1 0 0 0 1 0 1 0	←DB2
ge(.	Line $3 \rightarrow$	1 0 0 0 1 0 0 0 1 0 1 0	←DB3
page(X=0)	:	1 1 1 1 1 0 0 0 1 0 0 0	←DB4
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	←DB5
1st	:	$1 0 0 0 1 0 \dots 1 1 1 0 0 0$	←DB6
	Line $7 \rightarrow$	0 0 0 0 0 0 0 0 0 0 0 0 0	←DB7(MSB)
	Line $8 \rightarrow$	1 1 1 1 0 0 0 1 1 1 0 0	←DB0(LSB)
=1)	Line $9 \rightarrow$	$1 0 0 0 1 0 \dots 0 1 0 0 1 0$	←DB1
page(X=1)	Line $10 \rightarrow$	$1 0 0 0 1 0 \dots 0 1 0 0 1 0$	←DB2
ge(:	$1 1 1 1 0 0 \dots 1 1 1 0 1 0$	←DB3
pa		$1 0 0 0 1 0 \dots 0 1 0 0 1 0$	←DB4
q		$1 0 0 0 1 0 \dots 0 1 0 0 1 0$	←DB5
2nd	:	1 1 1 1 0 0 0 1 1 1 0 0	←DB6
	Line $15 \rightarrow$	0 0 0 0 0 0 0 0 0 0 0 0 0	←DB7(MSB)
	Line 56 \rightarrow		←DB0(LSB)
	:	1 0 0 0 1 0 0 0 0 0 0 0	←DB1
X=		1 0 0 0 1 0 0 1 0 0 1 0	←DB2
page(X=7)		1 1 1 1 1 0 1 0 1 0 1 0	←DB3
pa		1 0 0 0 1 0 1 0 0 1 0 0	←DB4
Ч	·	1 0 0 0 1 0 1 0 0 1 0 0	←DB5
8th	Line $62 \rightarrow$	1 0 0 0 1 0 0 1 1 0 1 0	←DB6
	Line $63 \rightarrow$	0 0 0 0 0 0 0	←DB7(MSB)

Relationship between RAM data and display



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ADDRESS CONFIGURATION OF DISPLAY DATA RAM

	X7 11	
0 1	Y address 2 3126	127
DB0	2 5120	Line0
$X=0 \rightarrow To$	Page0	То
DB7		Line7
DB0		Line8
$X=1 \rightarrow To$	Page1	То
DB7	e e	Line15
DB0		Line16
X=2→ To	Page2	То
DB7		Line23
DB0		Lint24
X=3→To	Page3	То
DB7		Line31
DB0		Line32
X=4→ To	Page4	То
DB7		Line39
DB0		Line40
$X=5 \rightarrow To$	Page5	To
DB7		Line47
DB0		Line48
$X=6\rightarrow To$	Page6	To
DB7		Line55
DB0		Line56
$X=7 \rightarrow To$	Page7	To
DB7		Line63

Address configuration of Display Data RAM



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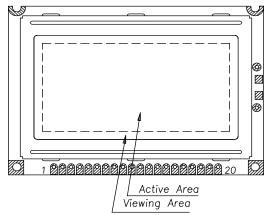
RELIABILITY TEST

No	Item	Conditions		Note
1.	High Temp. Operation	70°C	240 Hr	
2.	High Temp. Storage	80°C	240 Hr	
3.	Low Temp. Operation	-20°C	240 Hr	
4.	Low Temp. Storage	-30°C	240 Hr	
5.	High Temp./Humid Storage	40°C 90%RH	240 Hr	
6.	Thermal Shock	-20°C ,30min +40°C ,30min	10 cycles	
7.	Vibration Test (IEC-68-2-6)	Frequency : 10~55 Hz Duration : 20 times, 6 min/time Amplitude : 0.75 mm		
8.	Shock (IEC 68-2-27)	Duration : 11 mS Acceleration : 100g		X, Y, Z direction

APPEARANCE CHECK

CONDIITON OF APPEARANCE CHECK:

(1) Specimen shall be checked by eyes in distance of 30cm under 40w-fluorescence lamp.



(2) Checking direction shall be in 45 degree from perpendicular line op specimen surface.



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Due de et Sus e ifi e etien	Madali	NMTC C12964DEVIICCY D	Rev. No.	Issued Date.	Page.		
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HANDLING PRECAUTIONS

- (1)Treat polarizer very carefully since it is easy to be damaged.
- (2)When cleaning the display surface, use soft cloth (e.g. gauss) with a solvent (recommended below) and wipe lightly.
 - \blacklozenge ethyl alcohol
 - ♦ iso-prcolol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvents:

- water
- ♦ ketone
- aromatics
- (3)Direct current causes electro-chemical reaction with remarkable degradation of the display quality. Give careful consideration to prevent direct current at ON/OFF timing and during operation.
- (4)Avoid strong shock and drop from the height.
- (5)To prevent LCD panels from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.
- (6) Give careful consideration to avoid electrical static discharge with causes uneven contrast.
- (7)Even a small condensation on the contact pads (terminals) causes electro-chemical reaction which makes missing row and column. Give careful attention to avoid condensation. When assembling with zebra connector, clean the surface of the pads with alcohol and keep the air very clean.



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LCD PRODUCT QUALITY STANDARD DISPLAY APPEARANCE

No	ISPLAY APPEARANG	Criteria			
INO	Item				
1.	inclusions (black spot, white spot, dust)	(2) linear type length mm(l) width mm(W) no. of defect			
		$\begin{array}{cccccccccccccccccccccccccccccccccccc$			
2.	scratch	1. scratch on protective film is permitted.2. scratch on polarizer shall be as follow:(1) round typediameter mm(a*)no of defect $a \le 0.15$ neglect $0.15 < a \le 0.20$ 2 max $0.20 < a$ none(2) linear typebe judged bye 1(2) linear type			
3.	dent	diameter < 1.5mm			
4.	bubble	not exceeding 0.5mm average diameter is acceptable between glass and polarizing film			
5.	pin hole	$(a+b)/2 \le 0.15$ mm maximum number: ignored $0.15 < (a+b)/2 \le 0.20$ mm maximum number:10			
6.	dot defect	$(a+b)/2 \le 0.20$ mm maximum number: ignored $0.20 < (a+b)/2 \le 0.30$ mm maximum number:5 x=width			
7.	contrast irregularity(spot)	diameter specno of defect $a \le 0.50 \text{mm}$ neglect $0.50 < a \le 0.75$ 5 $0.75 < a \le 1.00$ 3 $1.00 < a$ none			
8.	dot width	design width $\pm 15\%$			
9.	color tone and uniformity	obvious uneven color is not permitted			



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WARRANTY

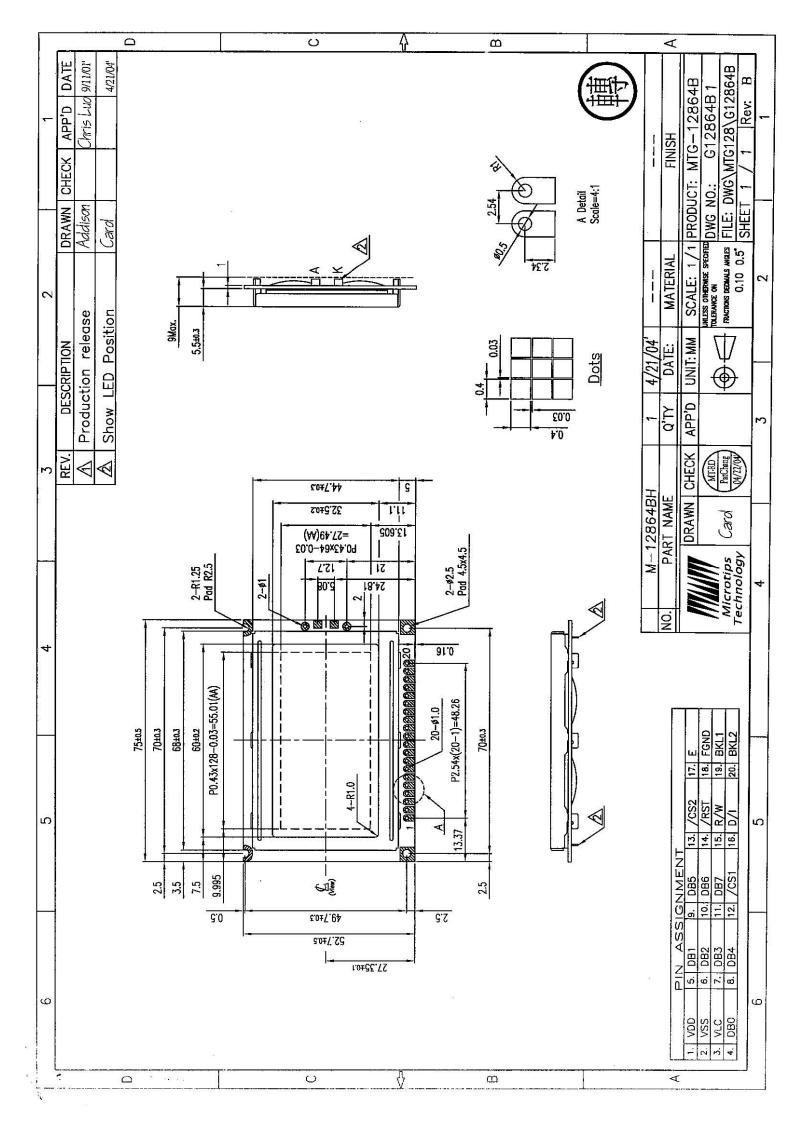
This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1 13 months guarantee starts from the date code.
- We cannot accept responsibility for any defect, which may arise from additional 2 manufacturing of the product (including disassembly and reassembly), after product delivery.
- We cannot accept responsibility for any defect, which may arise after the application 3 of strong external force to the product.
- 4 We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 5 We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.

DIMENSIONAL OUTLINES

• See the next page.....





Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Microtips Technology: NMTG-S12864BFYHSGY-D